

# BEST AVAILABLE COPY

PATENT NUMBER and  
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## U.S. UTILITY Patent Application

31017 U.S. PTO  
10/076355  
2/19/02

APPL NUM 10076355	FILING DATE 02/19/2002	CLASS 257	SUBCLASS 258	GAU 2811	EXAMINER H. V. W.
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\*\*APPLICANTS: Yamashita Tomio;

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:  
JAPAN 2001-053772 02/28/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 020167
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Semiconductor device and method for manufacturing semiconductor device		

U.S. DEPT. OF COM. / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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